



Abstract

HPX, a C++ runtime system for parallel and distributed programming, is designed to enhance computational efficiency on platforms ranging from multicore PCs to supercomputers. To showcase the portability and performance of HPX, we sought to build and run HPX applications on a cluster of Raspberry Pis. Running on the ARM architecture of a Raspberry Pi and accelerating computation on that architecture shows that HPX can be used to take advantage of even the simplest parallel and distributed hardware structures. Indeed, the HPX applications we tested, which estimate π using a Monte Carlo simulation, showed excellent parallel and distributed scaling on the Pi cluster.

Process

- Write a serial C++ application to estimate the value of π using a Monte Carlo simulation.
- 2. Build HPX for the Raspberry Pi and Raspbian OS by directly compiling dependencies and then cross-compiling HPX.
- Write a parallel implementation of the Monte Carlo method to 3. calculate π using HPX.
- 4. Test the scalability of the parallel HPX application on a single Raspberry Pi.
- 5. Write a parallel and distributed HPX application to estimate π .
- 6. Build a cluster of four Raspberry Pi 3s.
- 7. Measure the scalability of the parallel and distributed application on the Pi cluster.

Estimating π

Monte Carlo simulations can be used to estimate the value of π . By generating uniformly distributed points in a 1x1 square, we can estimate the area of a quarter circle with radius 1 by dividing the

number of these random points lying inside of the circle by the total number of points (see figure to the right). Since we know the area of a unit circle to be π , the area of the quarter circle is $\pi/4$, so we can multiply the estimate for the area by four to obtain an estimate of π .





Leveraging HPX on a Cluster of Raspberry Pis Jesse Goncalves¹, Dr. Hartmut Kaiser²

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Results

2	1.0E-04	1.0E-06
3	7.73E+04	7.73E+08
3	1.93E+04	1.93E+08
7	4.85E+03	4.85E+07

Our Monte Carlo HPX applications showed excellent parallel and distributed scalability on the Raspberry Pis, and the scaling efficiency in each case remained above 99% for all of the tests we ran. The scalability of our HPX applications can be observed in the graphs of trials per second vs. number of cores/nodes, a generic measure of scaling. The table of computation times required to reach different percent errors in our calculation of π also showcases the scalability of HPX. Building HPX for the Raspberry Pis proved to be the most challenging part of the project. We decided to cross-compile HPX because of how long it takes to build, which required a great deal of experimentation with a cross-compilation toolchain and the configuration options of HPX and its dependencies. The procedure we followed can certainly be improved upon in the future, but we have shown that HPX ports to the ARM architecture of the Pis.

- be added to the HPX documentation.

Acknowledgements

- project.
- Monte Carlo π estimation image: estimate-for-pi-with-numpy.html
- Raspberry Pilogo: Ο PGB001.png





Discussion

Next Steps

Add more Raspberry Pis to the cluster and test the distributed scaling of our HPX application on a larger number nodes. Optimize the process of building HPX for Raspbian so that others can easily follow the same procedure, which could then

Write and test more complicated HPX applications on the Pis.

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